

## 93410 TTL Isoplanar Memory 256-Bit Fully Decoded Random Access Memory

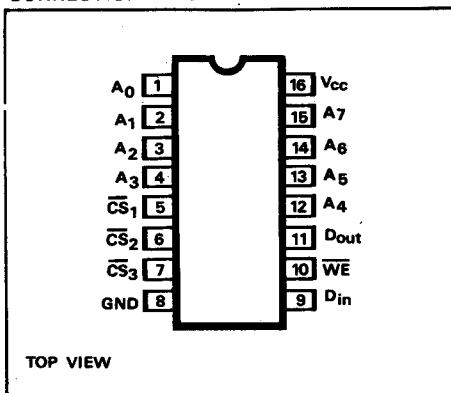
### GENERAL DESCRIPTION

The 93410 is a high speed 256-BitTTL random access memory with full decoding on chip. Each memory, organised as 256 words  $\times$  1 bit is designed for scratch pad, buffer and distributed main memory applications. The device has three chip select lines to simplify its use in larger memory systems. Address input pin locations are specifically chosen to permit maximum packaging density and for ease of PC board layout. An uncommitted collector output is provided to permit "OR-ties" for ease of memory expansion.

### FEATURES

Organisation – 256 words  $\times$  1 bit.  
Three high speed chip select inputs.  
93410 Commercial 45ns.  
Non inverted data output.  
On chip decoding.  
Power dissipation – 1.8mW/BIT

### CONNECTION DIAGRAM

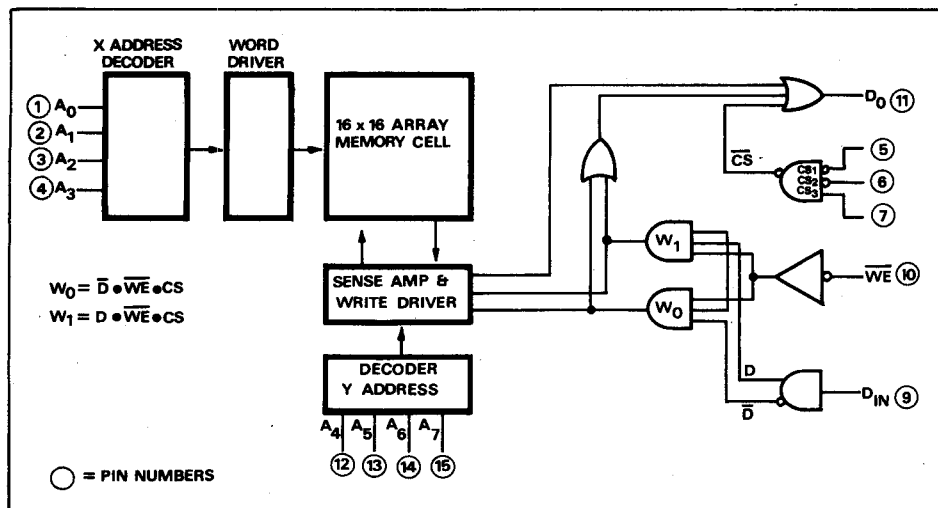


See outline drawings No. 141 and 137 for dimensions.

### REFERENCE TABLE

Code	Stock No.
93410DC	35780X
93410PC	35781R

### LOGIC DIAGRAM



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