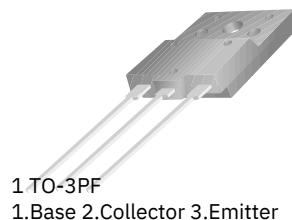


KSC5803

KSC5803

High Voltage Color Display Horizontal Deflection Output (No Damper Diode)

- High Breakdown Voltage : BV=1500V
- High Speed Switching : $t_r=0.1\mu s$ (Typ.)
- Wide S.O.A
- For C-Monitor(85KHz)



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
VCBO	Collector-Base Voltage	1500	V
VCEO	Collector-Emitter Voltage	800	V
VEBO	Emitter-Base Voltage	6	V
IC	Collector Current (DC)	12	A
ICP	Collector Current (Pulse)	24	A
PC	Collector Dissipation (TC=25°C)	70	W
TJ	Junction Temperature	150	°C
TSTG	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
ICES	Collector Cut-off Current	$V_{CE} = 1400V, V_{BE} = 0$			1	mA
ICBO	Collector Cut-off Current	$V_{CE} = 800V, I_E = 0$			10	μA
IEBO	Emitter Cut-off Current	$V_{EB} = 4V, I_C = 0$			1	mA
hFE1	DC Current Gain	$V_{CE} = 5V, I_C = 1A$	15		40	
hFE2		$V_{CE} = 5V, I_C = 8A$	5		8.5	
VCE(sat)	Collector-Emitter Saturation Voltage	$I_C = 8A, I_B = 2A$	5		3	V
VBE(sat)	Base-Emitter Saturation Voltage	$I_C = 8A, I_B = 2A$			1.5	V
tSTG	Storage Time	$V_{CC} = 200V, I_C = 7A$			4	μs
tF	Fall Time	$I_{B1} = 1.4A, I_{B2} = -2.8A$ $R_L = 28.6\Omega$			0.3	μs

Typical Characteristics

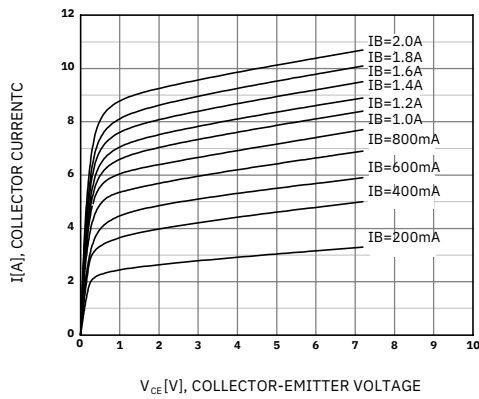


Figure 1. Static Characteristic

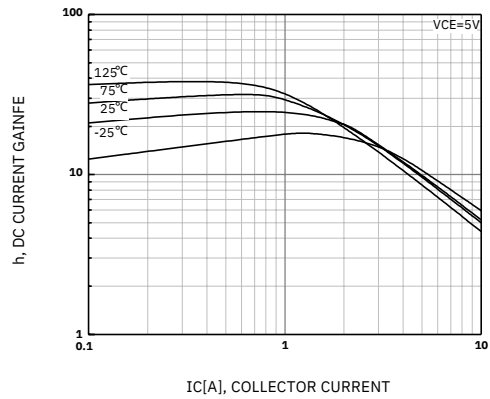


Figure 2. DC current Gain

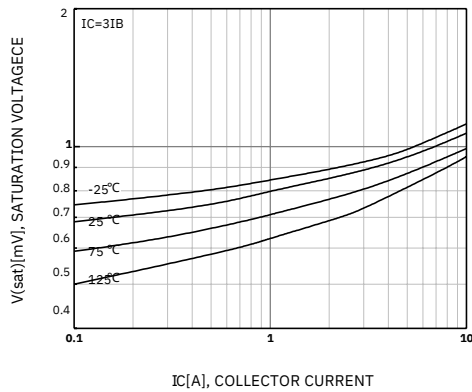


Figure 3. Base-Emitter Saturation Voltage

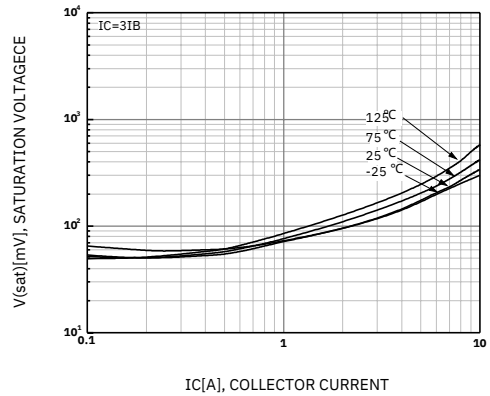


Figure 4. Collector-Emitter Saturation Voltage 1

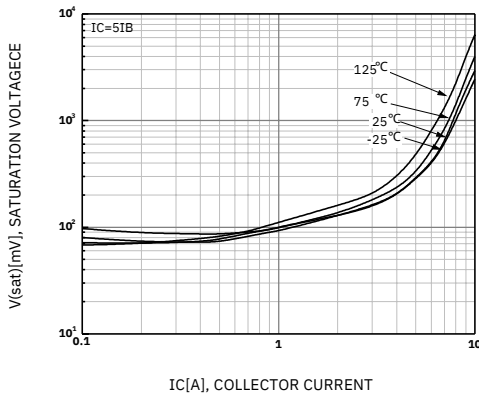


Figure 5. Collector-Emitter Saturation Voltage 2

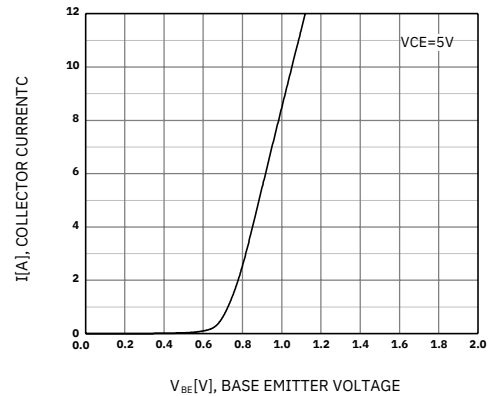


Figure 6. Base-Emitter On Voltage

Typical Characteristics (Continued)

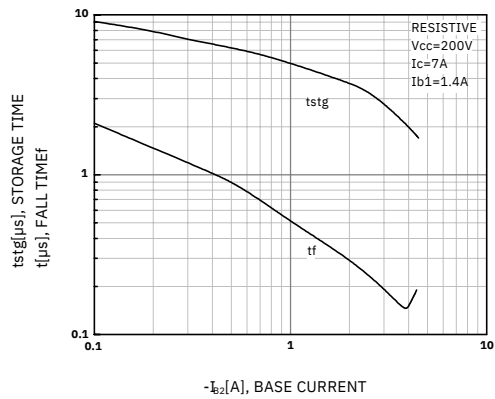


Figure 7. Switching Time

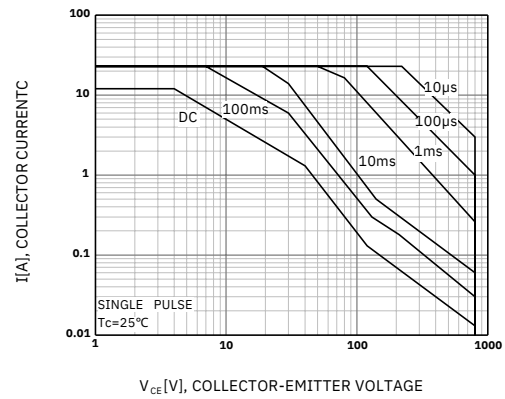


Figure 8. Safe Operating Area

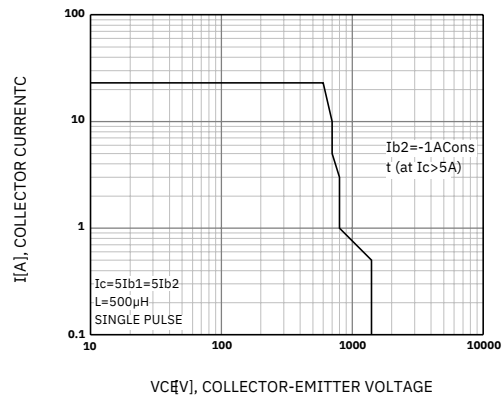


Figure 9. Reverse Bias Safe Operating Area

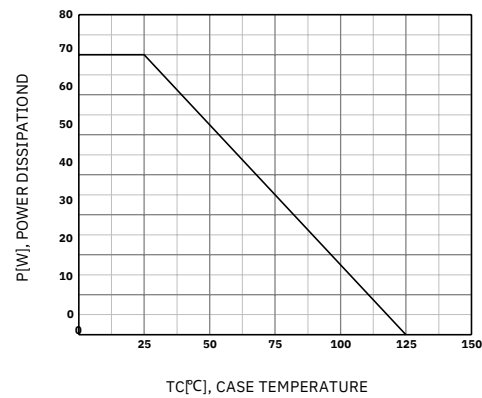


Figure 10. Power Derating

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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