XILINX JTAG SMT DIGILENT USB

Xilinx Download Line Xilinx Platform Cable USB Downloader CPLD/FPGA Emulator



Features:

* High speed and stability, complete functions .

JTAG download rate up to 30M, improve work efficiency and save waiting time;

Support Vivado, ISE, SDK, IMPACT, Chipscope, EDK, etc.

(ISE12.1+, Vivado12.1+ Cable plug-in support) It is recommended that ISE use version 14.1 or higher (with its own driver).

* Support updated and upgraded devices

Support all FPGA chips

- >Zynq-7000 series
- >Kintex UltraScale+
- >Virtex UltraScale+
- >Artix-7
- >Kintex-7

- >Virtex-7
- >Virtex-6
- >Spartan-6
- + CoolRunner-II CPLD series
- + Xilinx Platform Flash ISP configuration PROM series
- + Third party SPI PROMs
- + Third party SPI BPI PROMs

9500/9500XL CPLD and 1700 and 18V00 ISP PROM devices are not supported.

* The following operating systems are supported:

- —Microsoft Windows 7\Windows 8\Windows 10
- -Microsoft Windows XP Professional -Windows 2000 -Red

Hat Enterprise Linux

—SUSE Linux Enterprise

* Automatically judge and adapt to the target I/O voltage

* Compatible with the indicated device voltage range:

5V (TTL), 3.3V (LVCMOS), 2.5V, 1.8V and 1.5V; the reference voltage is consistent with the official one, and it has the function of preventing wrong insertion or high voltage damage to the downloader and board.

Shipping list:

High configuration:

- 1. One Xilinx SMT2 high speed USB downloader
- 2.One USB printing cable
- 3. One multi-function adapter board
- 4. One 14-pin 20cm long cable spacing 2.0mm
- 5. One 14-pin 20cm long cable spacing 2.54mm
- 6. 10-needle 20cm long line spacing 2.54mm One piece of
- 7, 7 core hole 20cm long Dupont line spacing 2.54mm One piece of

- 8, 7 core hole 20cm Dupont line spacing 2.54mm (with signal logo) One piece
- 9. Data network disk:

Standard configuration:

- 1. One Xilinx SMT high-speed USB downloader
- 2. One USB2.0 data cable
- 3. One 2.54mm-14P cable
- 4. Data network disk

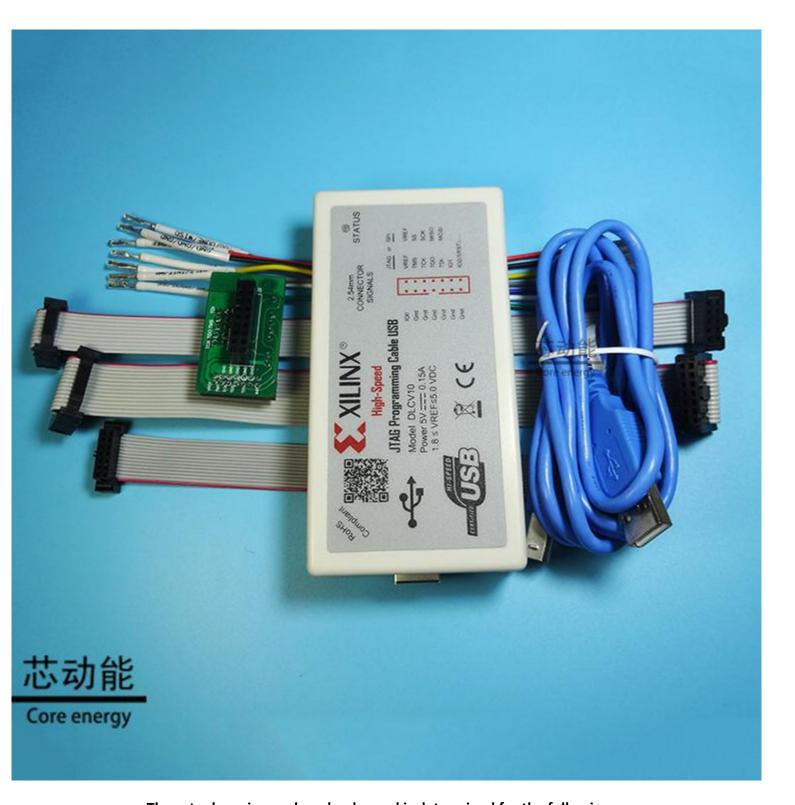
XILINX JTAG DIGILENT USB



- 1. All Xilinx FPGAs.
- 2. Xilinx Zynq-7000
- 3. Xilinx CoolRunner™/CoolRunner-II CPLDs
- 4. Xilinx Platform Flash ISP configuration PROMs
- 5. Select third-party SPI PROMs
- 6. Select third-party BPI PROMs

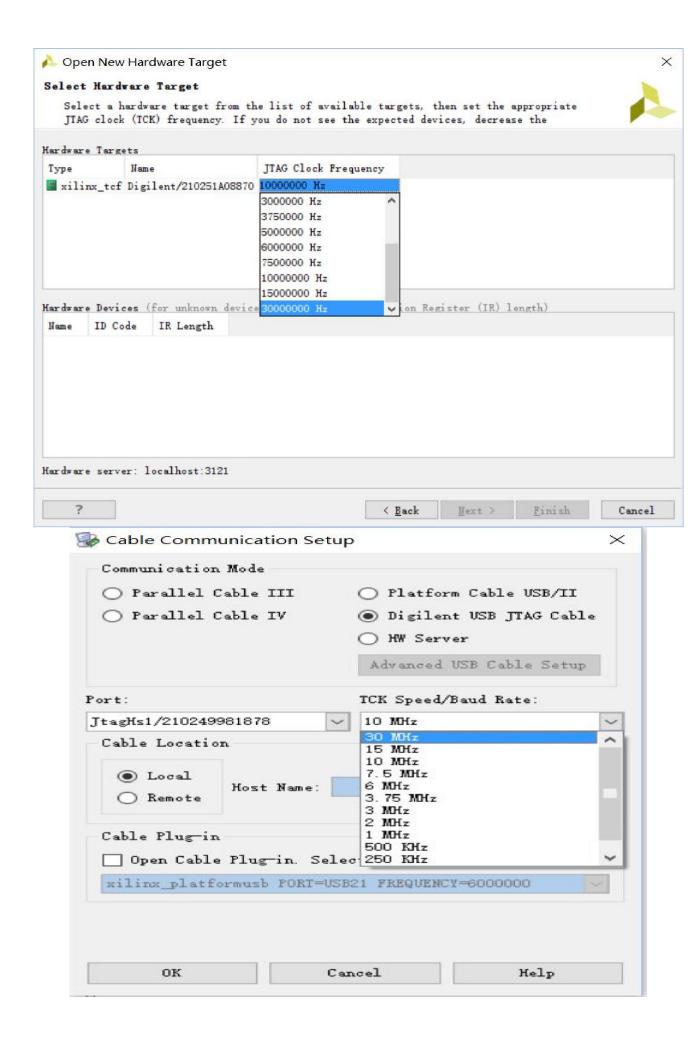


- 1. Ordered List Item Xilinx 9500/9500XL CPLDs
- 2. Xilinx 1700 and 18V00 ISP configuration PROMs
- 3. Xilinx FPGA eFUSE programming



The actual maximum download speed is determined for the following reasons:

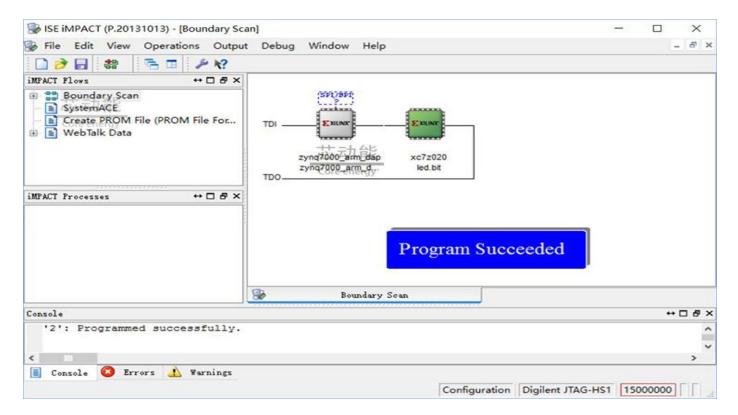
(1) Set the required download speed in the xilinx software: the default configuration of the software is generally 10MHz; you can try to increase it. When the speed is not applicable to the target device, please slow down the software; as shown below:



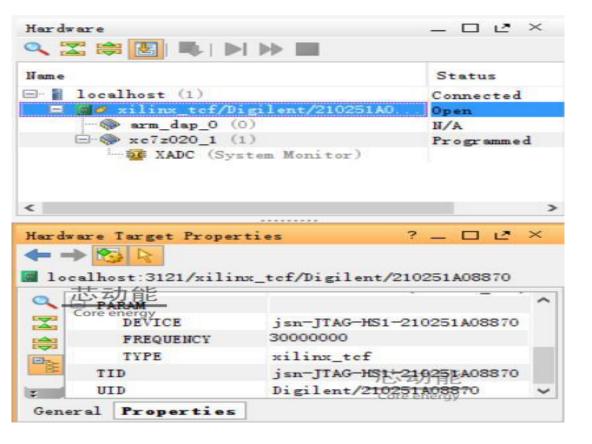
(2) The signal integrity on the link of the target device itself needs to support the selected rate;

Speed measured results:

impact: 15MHZ



Vivado: 30MHZ

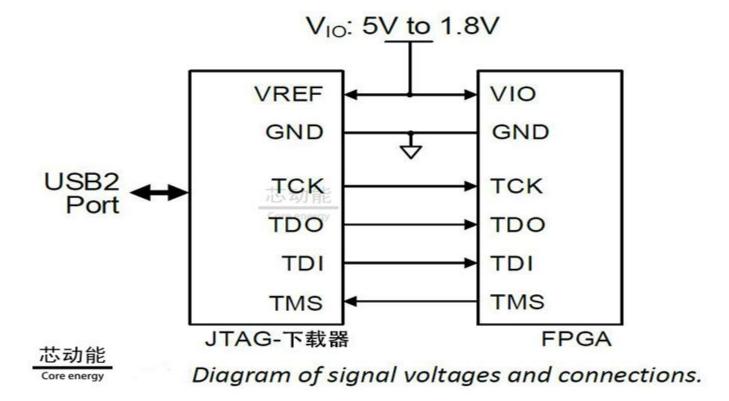


Selection guide:

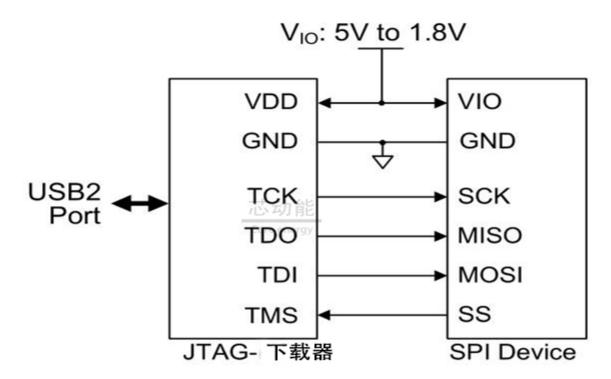
选择指南 (Selection Guide)	MT2	HS3	HS2	芯HSI能
最大速度 (Max Speed)	30 Mhz	30 Mhz	30 Mhz	30 Mhz
电压范围 (Voltage Range)	1.8 - 5 V			
Xilinx原生支持 (Xilinx Native Support)	ISE 14.1+ Vivado 2013.1+	ISE 14.1+ Vivado 2013.1+	ISE 13.2+ Vivado 2012.1+	ISE 13.2+ Vivado 2012.1+
Xilnx插件支持 (Xilinx Plug-in Support)	ISE 13.1+	ISE 12.1+	ISE 12.1+	ISE 12.1+
电脑接口 (PC Interface)	USB	USB	USB	USB
Digilent Adept支持 (Digilent Adept Support)	ок	ок	ок	ок
4线JTAG (4-Wire JTAG)	ок	ок	ок	ОК
2线JTAG (2-Wire JTAG)	ок	NO	ок	NO
ZYNQ-7000 PS_SRST Support (ZYNQ-7000 PS_SRST支持)	ок	ок	NO	NO
CoreSPI 支持 (SPI Support)	ок	NO	ок	ок

The Xilinx JTAG Downloader is connected to the target board:

1. JTAG connection.



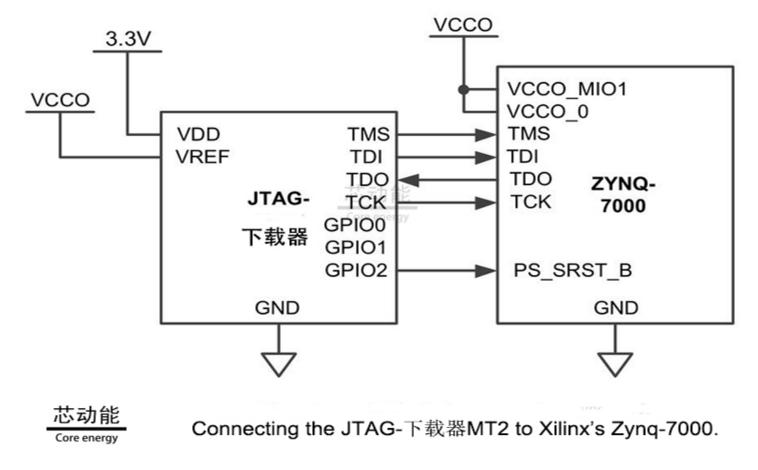
2. SPI connection.



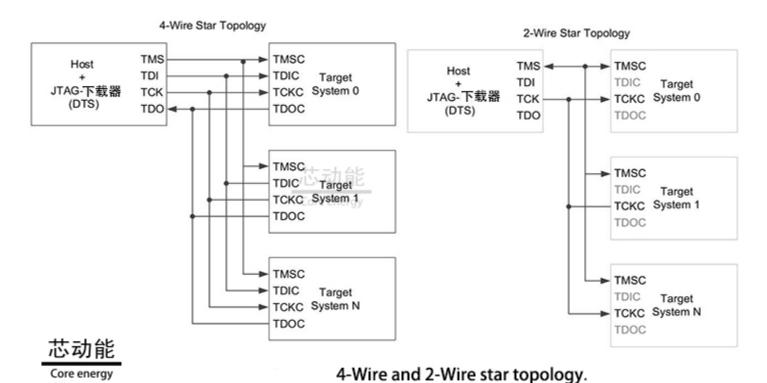
芯动能 Core energy

SPI Device Connections

3. Xilinx's Zynq-7000 PS_SRST_B connection.



4-Wire and 2-Wire star topology.



Shipping list:

Standard version
Xilinx JTAG Downloader 1 piece
USB2.0 line 1 piece
2.54mm 14P cable 1 piece

Set version:

Xilinx JTAG Downloader 1 piece USB2.0 line 1 piece Adapter plate 1 piece 2.0mm 14P cable 1 piece 2.54mm 10P cable 1 piece

Color cable 1 piece