

# BS813A-1/BS814A-1 BS814A-2/BS816A-1/BS818A-2 Touch Key

#### **Features**

- Operating voltage: 2.2V~5.5V
- · Low standby current
- · Auto-calibration
- · Reliable touch detections
- · High power supply rejection
- Standby and normal operating modes
- · Maximum key on duration time detection
- · Adaptive voltage drop function
- · Level Hold, selectable active level-low or high
- NMOS output with internal pull-high/CMOS-direct output
- Both serial interface and parallel outputs
- · Sensitivity adjustment using an external capacitor
- · Minimal number of external components

### **General Description**

The BS81x is a series of  $3\sim 8$  key touch key devices which can detect human body contact using external touch pads. The high level of device integration enable applications to be implemented with a minimum number of external components.

The BS81x series devices are equipped with serial or parallel interfaces to allow easy communication with an external MCU for device setup and for touch pin monitoring purposes. Special internal circuitry is also employed to ensure excellent power noise rejection to reduce the possibility of false detections, increasing the touch switch application reliability under adverse environmental conditions.

With auto-calibration, low standby current, excellent resistance to voltage fluctuation and other features, this range of touch key devices provide a simple and effective means of implementing touch key operation in a wide variety of applications.

### **Selection Table**

For this device series, most of the feature are similar. The BS814A-2 and BS818A-2 include a serial interface function while the BS813A-1, BS814A-1 and BS816A-1 have parallel outputs. The BS816A-1 and BS818A-2 also include two options. The following table summarizes the main characteristics of each device.

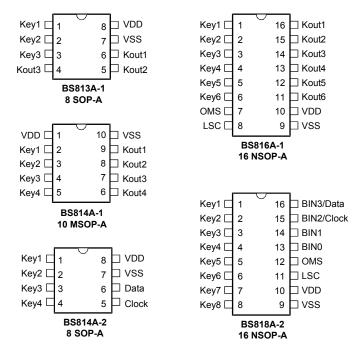
Part. No.	Touch Key	OPTION	lstb@3V	Parallel Outputs	Serial Interface	Power Mode	Auto Calibration	Package
BS813A-1	3-Key	×	4.5μΑ	NMOS	×		√	8SOP
BS814A-1	4-Key	×	5.0μΑ	(internal pull-high)	×		√	10MSOP
BS814A-2	4-Key	×	5.0μΑ	×	√	Normal	√	8SOP
BS816A-1	6-Key	OMS/LSC (Note *)	12μΑ/6μΑ	NMOS (internal pull-high)/ CMOS-Direct	×	and Standby	√	16NSOP
BS818A-2	8-Key	OMS/LSC (Note *)	12μΑ/6μΑ	Binary	√		√	16NSOP

Note: \* OMS and LSC refer to the option table.

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### **Pin Assignment**



### **Pin Description**

#### BS813A-1

Pin name	I/O	Description
Key1~Key3	Input	Touch key input pin, unused touch keys require grounding
Kout1~ Kout3	NMOS output	Touch key output pin with internal pull high
VSS	_	Negative power supply, ground
VDD	_	Positive power supply

### BS814A-1

Pin name	I/O	Description
Key1~Key4	Input	Touch key input pin, unused touch keys require grounding
Kout1~ Kout4	NMOS output	Touch key output pin with internal pull high
VSS	_	Negative power supply, ground
VDD	_	Positive power supply

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### BS814A-2

Pin name	I/O	Description
Key1~Key4	Input	Touch key input pin, unused touch keys require grounding
Clock	Input	2-wire series interface Clock input with internal pull high
Data	NMOS output	2-wire series interface Data NMOS output with internal pull high
VSS	_	Negative power supply, ground
VDD	_	Positive power supply

### BS816A-1

Pin name	I/O	Description	
Key1~Key6	Input	Touch key input pin, unused touch keys require grounding	
OMS	Input	Output Mode Selection. Open=NMOS output (low active) Grounded=CMOS-Direct output (high active)	
LSC	Input	Power-saving mode selection Open= General power-saving mode; refer to D.C. Characteristics I <sub>STB3</sub> . Grounded=More power-saving mode; refer to D.C. Characteristics I <sub>STB4</sub> .	
Kout1~ Kout6	NMOS output	OMS open, NMOS output with internal pull-high.	
Rout 1~ Routo	CMOS output	OMS grounded, CMOS-Direct output	
VSS	_	Negative power supply, ground	
VDD	_	Positive power supply	

#### BS818A-2

Pin name	I/O	Description
Key1~Key8	Input	Touch key input pin, unused touch keys require grounding
OMS	Input	Output Mode Selection. Open = 2-wire serial mode Grounded = 4-wire binary parallel mode
LSC	Input	Power-saving mode selection Open=General power-saving mode; refer to D.C. Characteristics I <sub>STB3</sub> . Grounded=More power-saving mode; refer to D.C. Characteristics I <sub>STB4</sub> .
Clock	Input	OMS open,2-wire series interface Clock input - internal pull- high
Data	NMOS output	OMS open, series interface Data NMOS output - internal pull-high
BIN3~ BIN0	CMOS output	OMS grounded, Binary output mode
VSS	_	Negative power supply, ground
VDD	_	Positive power supply

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### **Absolute Maximum Ratings**

Supply VoltageV <sub>SS</sub> -0.3V	$\sim V_{SS} + 6.6V$	Storage Temperature	$\text{-}50^{\circ}\text{C} \sim 125^{\circ}\text{C}$
Input VoltageV <sub>SS</sub> -0.3V	$\sim V_{DD} + 0.3V$	Operating Temperature	40°C ~ 85°C
I <sub>OL</sub> Total	80mA	Total Power Dissipation	500mW
I Total	00m A		

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to these devices. Functional operation of these devices at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect devices reliability.

### **D.C. Characteristics**

Council of	Downworks.	Parameter Test Conditions Min. Typ.		T			
Symbol	Parameter	<b>V</b> <sub>DD</sub>	V <sub>DD</sub> Conditions		Тур.	Max.	Unit
$V_{DD}$	Operating Voltage	_	_	2.2	_	5.5	V
	Operating Current	3V	No load	_	0.8	1.5	mA
I <sub>DD</sub>	Operating Current	5V	No load	_	1.5	3.0	mA
	Standby Current (BS813A-1)(Note 1)	3V	No load	_	4.5	_	mA
I <sub>STB1</sub>	Standby Current (BS613A-1)(Note 1)	5V	No load	_	9	_	mA
	Standby Current (BS814A-1/BS814A-2)	3V	No load	_	5.0	_	mA
I <sub>STB2</sub>	(Note 1)	5V	No load	_	10	_	mA
	Standby Current (BS816A-1/BS818A-2)	3V	3V 5V No load	_	12	_	mA
I <sub>STB3</sub>	(Note 1) LSC=Open (Note 2)	5V		_	25	_	mA
	Standby Current (BS816A-1/BS818A-2)	3V		_	6.0	_	mA
I <sub>STB4</sub>	(Note 1) LSC=V <sub>SS</sub> (Note 2)	5V	No load	_	12	_	mA
		5V	_	0	_	1.5	V
$V_{IL}$	Input Low Voltage for Clock pin	_	_	0	_	0.2V <sub>DD</sub>	V
\ /	In most High Valtage for Class, min	5V	_	3.5	_	5.0	V
V <sub>IH</sub>	Input High Voltage for Clock pin	_	_	0.8V <sub>DD</sub>	_	V <sub>DD</sub>	V
	Kaut/Data Sink Compart (NIMOS)	3V	V <sub>OL</sub> =0.1V <sub>DD</sub>	4	8	_	mA
I <sub>OL</sub>	Kout/Data Sink Current (NMOS)	5V	V <sub>OL</sub> =0.1V <sub>DD</sub>	10	20	_	mA
1	BIN0~BIN3 Source Current (CMOS)	3V	V <sub>OH</sub> =0.9V <sub>DD</sub>	-2	-4	_	mA
I <sub>OH</sub>	BINO-BING Source Current (CMOS)	5V	V <sub>OH</sub> =0.9V <sub>DD</sub>	-5	-10	_	mA
R <sub>PH</sub>	Pull-high Resistance to Kout	3V	_	20	60	100	kΩ
I <b>√</b> PH	i dii-iligii Nesistalice to Rout	5V	_	10	30	50	kΩ

Note: 1.  $I_{\text{STB}}$  is the average standby current.

2. LSC instruction refer to the Option table.

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# A.C. Characteristics

Ta =25°C

Councile of	vmbol Parameter		Test Conditions	Min.	T	Max.	Unit
Symbol	Parameter	V <sub>DD</sub> Conditions		IVIIII.	Тур.	wax.	Unit
t <sub>KRT1</sub>	Key Response TimeNormal Mode	_	_	100	125	150	ms
t <sub>KRT2</sub>	Key Response TimeStandby Mode BS813A-1 / BS814A-1 / BS814A-2	_	_	100	150	250	ms
	Key Response TimeStandby Mode	_	LSC=Open	100	150	250	ms
t <sub>KRT3</sub>	BS816A-1 / BS818A-2	_	LSC=V <sub>SS</sub>	400	600	1000	ms
t <sub>KH</sub>	Maximum Key Hold Time	_	_	60	64	68	s
t <sub>CAL1</sub>	Auto-Calibration Period - Normal Mode	_	_	_	1	_	s
t <sub>CAL2</sub>	Auto-Calibration Period - Standby Mode	_	_	_	32	_	S
t <sub>NS</sub>	Normal to Standby Mode conversion time	_	_	7	8	9	S

### **Serial interface Characteristics**

Cumbal	Parameter	,	Test Conditions	Min.	Balina Trum		Unit
Symbol	Parameter	V <sub>DD</sub>	Conditions	WIII.	Тур.	Max.	Unit
T <sub>START</sub>	Start bit low time	_	_	_	_	t <sub>NS</sub>	s
T <sub>LOW</sub>	Clock low time	_	_	20	_	_	us
T <sub>HIGH</sub>	Clock high time	_	_	20	_	_	us
T <sub>BR</sub>	Data transfer rate	_	_	_	_	25	Kbps
T <sub>ED</sub>	Delay time between a error reading and the next reading	_	_	6	_	_	ms

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### **Functional Description**

#### Introduction

This range of BS81x touch key devices offer an easy and reliable means of implementing touch switch functions in a wide range of applications which require 3~8 touch keys. Very few external components are required for full touch key implementations. In addition to simple parallel output, a two-wire serial interface and binary output offer a convenient communication with an external MCU. Sensitivity adjustment is also an easy matter. By the simple connection of a small capacitor to the touch key input pin, the changes in the capacitor value will be reflected in different sensitivity values. By having a fully integrated adaptive voltage drop function, touch switch applications can save on the usually required LDO.

### **Option Table**

The BS816A-1 and BS818A-2 provide two options to enhance application flexibility.

#### **OMS**

	OMS - Output Mode Selection	Description
BS816A-1	Open	NMOS output with internal pull- high, low active
	Vss	CMOS output, high active
	Open	2-wire serial mode
BS818A-2	Vss	4-wire Binary parallel mode

#### LSC

	LSC- Lower Standby Current	Description
DC016A 1/	Open	General power- saving
BS816A-1/ BS818A-2	Vss	More power-saving (wake-up time of 0.5 ~1s)

### **Operating Mode**

There are two operating modes for this device series, the normal mode and the standby mode. During the 8 seconds after power-on, if no touch key actions are detected, the devices will automatically enter the standby mode to reduce their power consumption. If a key or keys are pressed, the device will be woken up and will then enter the normal mode and output the key state value until all keys are released. After 8 seconds, the system will then revert to the standby mode again.

#### **Touch Key Outputs**

For the BS813A-1 and BS814A-1 devices, all output pins are NMOS structures with connected internal pull-high resistors. The non touch detect output logic level is high. When a touch action is detected, the corresponding output will change to a low state. For the BS816A-1 device, except the NMOS output type, users can also select a CMOS output type. The non touch detect output logic level is low. When a touch action is detected, the corresponding output will change to a high state.

#### **Serial Interface**

Both the BS814A-2 and BS818A-2 are equipped with a serial interface allowing for easy interfacing to an external master MCU. When the device detects that a touch key has been pressed, it will output a low on the Data pin which can be used to wake up the master MCU. After receiving this low level, the master can then send a clock signal to the Clock pin and read back the key status from the Data pin.

When a low clock signal is received the key status data is prepared by the touch key device. When the clock signal changes to a high level, the master reads the touch key status data from the Data line. The timings associated with the communication protocol can be fully described within 8 clock periods. Without waiting for a start bit, the touch key status condition can also be directly read using a polling method. If there are any errors in reading the data, it is necessary to wait for about 6ms and then read the data again.

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Data Transfer Timing - BS814A-2

#### 4-key Data Format

After a clock signal is received on the Clock pin, an 8-bit data byte will be generated by the touch key device and shifted out on the Data pin. Data bits, bit6~bit4, will also generate a checksum whose content informs how many touch keys have been touched. For example, if the check sum is equal to "010", it means that two keys have been touched. As to which keys are actually touched, this information can be retrieved from the condition of data bits, bit3~bit0. The state of the data bits, bit3~bit0, is used to indicate which touch keys, key4~key1, are touched or not respectively. A low bit means the corresponding key is touched. Otherwise, the key is not touched if the corresponding data condition is high.

Start bit: When a key changes state, the Data pin outputs a low, which can wake up the master, which can then read the key status.

Bit0: Key1 state - "0" = touch, "1" = no touch

Bit1: Key2 state - "0" = touch, "1" = no touch

Bit2: Key3 state - "0" = touch, "1" = no touch

Bit3: Key4 state - "0" = touch, "1" = no touch

Bit6~4: Checksum – the total number of "0"s is used to indicate how many keys have been touched.

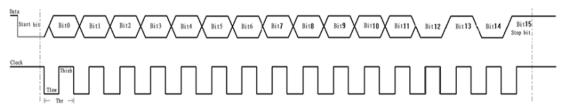
Bit7: Stop bit, always a "1", high level.

Bit	7	6~4	3~0	Descriptions	
Function	Stop bit	Check Sum	Key4 ~Key1 state	0: touched / 1: not touched	
	1	100	100	0000	Check Sum = 100, Four keys are touched.
		011	0001, 0010, 0100 or 1000	Check Sum = 011, Three keys are touched.	
Data Stream		010	0011, 0101, 0110, 1001, 1010 or 1100	Check Sum = 010, Two keys are touched.	
		001	0111, 1011, 1101 or 1110	Check Sum = 001, One key is touched.	
		000	1111	Check Sum = 000, No key is touched.	

4-key Serial Data Stream Format

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Data Transfer Timing - BS818A-2

#### 8-key Data Format

After a clock signal is received on the Clock pin, a 16-bit data byte will be generated by the touch key device and shifted out on the Data pin. Data bits, bit11~bit8, will also generate a checksum whose content informs how many touch keys have been touched. For example, if the check sum is equal to "0010", it means that two keys have been touched. As to which keys are actually touched, this information can be retrieved from the condition of data bits, bit7~bit0. The state of the data bits, bit7~bit0, is used to indicate which touch keys, key8~key1, are touched or not respectively. A low bit means the corresponding key is touched. Otherwise, the key is not touched if the corresponding data condition is high.

Start bit: When a key state is changed, Data pin outputs a low, which can wake up the master, which can then read the key status.

Bit0: Key1 state - "0" = touch, "1" = no touch

Bit1: Key2 state - "0" = touch, "1" = no touch

Bit2: Key3 state - "0" = touch, "1" = no touch

Bit3: Key4 state - "0" = touch, "1" = no touch

Bit4: Key5 state - "0" = touch, "1" = no touch

Bit5: Key6 state - "0" = touch, "1" = no touch

Bit6: Key7 state - "0" = touch, "1" = no touch

Bit7: Key8 state - "0" = touch, "1" = no touch

Bit11~8: Checksum – the total number of "0"s is used to indicate how many keys have been touched.

Bit15~12: Stop bits, always "1010B".

Bit	15~12	11~8	7~0	Descriptions	
Function	unction Stop Check bit Sum Key8 ~Key1 state		Key8 ~Key1 state	0: touched / 1: not touched	
		1000	00000000	Check Sum = 1000, 8 keys are touched.	
	1010	0111	00000001, 00000010, 00000100, 00001000 00010000, 00100000, 01000000 or 10000000	Check Sum = 0111, 7 keys are touched.	
			0110	00000011,00000110,00011000,00110000 11000000 or 10000001	Check Sum = 0110, 6 keys are touched.
		0101	00000111,00001110,11100000,10000011, 10000110, 10001100 or 10011000	Check Sum = 0101, 5 keys is touched.	
Data Stream		0100	00001111,00011110,00111100,01111000, 11110000 or 10000111	Check Sum = 0100, 4 keys is touched.	
		0011	00011111,00111110,011111100,11111000 100011111 or 110001111	Check Sum = 0011, 3 keys are touched.	
		0010	00111111,01111110,11111100, 01111110, 01111101or 01111011	Check Sum = 0010, 2 keys are touched.	
		0001	11111110, 11111101, 11111011, 11110111 11101111, 11011111, 10111111 or 01111111	Check Sum = 0001, 1 key is touched.	
		0000	11111111	Check Sum = 0000, No key is touched.	

Serial Data Stream Format for 8-key

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#### **Parallel Interface**

The BS818A-2 also provides a parallel interface function which shows directly the key state condition. When the parallel output type is selected, the touch key data reflects only one key state, not more keys, at the same time. When no touch key is pressed, BIN3~BIN0 are high. When any key is pressed, BIN3 must output a low to wake up the master while BIN2~BIN0 reflect which keys are pressed as shown in the following table.

Pressed key	BIN3	BIN2	BIN1	BIN0
No key	1 (high)	1	1	1
Key1	0 (low)	1	1	1
Key2	0	1	1	0
Key3	0	1	0	1
Key4	0	1	0	0
Key5	0	0	1	1
Key6	0	0	1	0
Key7	0	0	0	1
Key8	0	0	0	0

#### **Maximum Key On Duration Time**

To minimise the possibility of unintentional switch detections, such as undesired objects covering the sensing electrodes, the devices include a Maximum Key On duration time function. To implement this function the devices include an internal timer, which starts running after each switch detection. If the key on time of a touch key exceeds a value of about 64 seconds, then the device will re-calibrate the key state, obtain a new reference value, while the output status is reset to the initial state.

#### **Auto-calibration Function**

The devices include a full auto-calibration function which will be initiated after the device is powered-on. In addition to the power-on calibration, if no switch detection has been made for about more than 1 second in the normal mode or 32 seconds in the standby mode, then a further calibration procedure will be carried out. By implementing this feature, changes in the touch key environmental conditions are automatically catered for dynamically.

### **Adaptive Voltage Drop**

This series of touch key devices include an adaptive voltage drop function which prevent touch key malfunction due to power supply voltage variations which may be caused by high current switching. With the adaptive voltage drop function, there is no need for an external LDO to deal with these voltage drop issues

### **Sensitivity Adjustment**

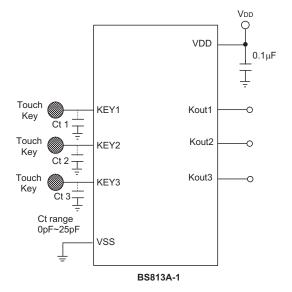
The sensitivity of the switch is a very important consideration in most applications whose requirements will vary according to the user application. The user should therefore be aware of the methods which can adjust the sensitivity of their touch key application. Changing the PCB electrode size, the conductive layout area below the electrode and the thickness of the dielectric material panel can all be used to adjust the touch switch sensitivity. Additionally for this device series, an external capacitor connected to the touch key input pin can also be used to adjust the sensitivity for different applications. Note that adding an external capacitor to the touch key input pin will reduce the sensitivity.

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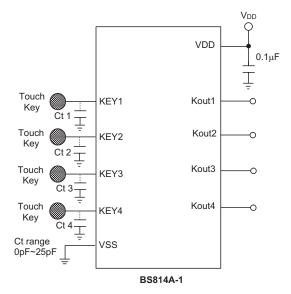


### **Application Circuits**

### BS813A-1



#### BS814A-1



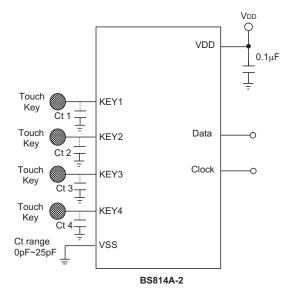
Note: 1. Ct (C threshold) is used for adjustment of Trigger Threshold. Recommended value: 0~25 pF

2. Ct value can be changed to obtain different sensitivity values. Higher Ct values will result in lower sensitivity levels. (0pF = no Ct)

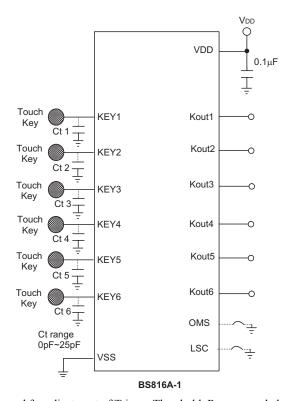
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#### BS814A-2



### BS816A-1



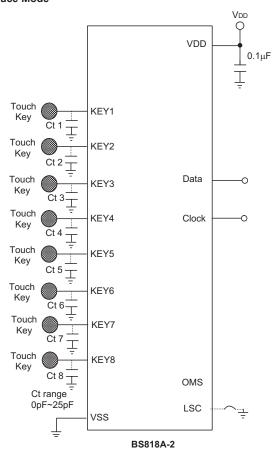
Note: 1. Ct (C threshold) is used for adjustment of Trigger Threshold. Recommended value:  $0\sim25~pF$ 

2. Ct value can be changed to obtain different sensitivity values. Higher Ct values will result in lower sensitivity levels. (0pF = no Ct)

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#### BS818A-2 - Serial Interface Mode



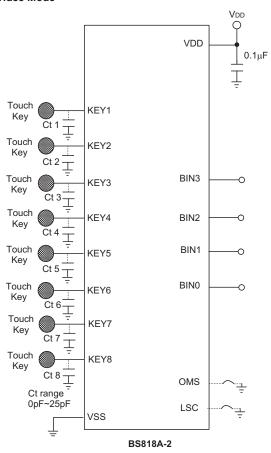
Note: 1. Ct (C threshold) is used for adjustment of Trigger Threshold. Recommended value:  $0\sim25~pF$ 

2. Ct value can be changed to obtain different sensitivity values. Higher Ct values will result in lower sensitivity levels. (0pF = no Ct)

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#### BS818A-2 - Parallel Interface Mode



Note: 1. Ct (C threshold) is used for adjustment of Trigger Threshold. Recommended value: 0~25 pF

2. Ct value can be changed to obtain different sensitivity values. Higher Ct values will result in lower sensitivity levels. (0pF = no Ct)

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### **Package Information**

Note that the package information provided here is for consultation purposes only. As this information may be updated at regular intervals users are reminded to consult the <u>Holtek website</u> for the latest version of the package information.

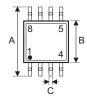
Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

- Further Package Information (include Outline Dimensions, Product Tape and Reel Specifications)
- Packing Meterials Information
- Carton information

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### 8-pin SOP (150mil) Outline Dimensions







Cumbal	Dimensions in inch			
Symbol	Min.	Nom.	Max.	
A	_	0.236 BSC	_	
В	_	0.154 BSC	_	
С	0.012	_	0.020	
C'	_	0.193 BSC	_	
D	_	_	0.069	
E	_	0.050 BSC	_	
F	0.004	_	0.010	
G	0.016	_	0.050	
Н	0.004	_	0.010	
α	0°	_	8°	

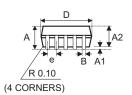
Cumbal	Dimensions in mm			
Symbol	Min.	Nom.	Max.	
A	_	6.0 BSC	_	
В	_	3.9 BSC	_	
С	0.31	_	0.51	
C'	_	4.9 BSC	_	
D	_	_	1.75	
E	_	1.27 BSC	_	
F	0.10	_	0.25	
G	0.40	_	1.27	
Н	0.10	_	0.25	
α	0°	_	8°	

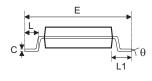
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### 10-pin MSOP Outline Dimensions







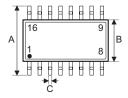
O: week al	Dimensions in inch			
Symbol	Min.	Nom.	Max.	
A	_	_	0.043	
A1	0.000	_	0.006	
A2	0.030	0.033	0.037	
b	0.007	_	0.013	
С	0.003	_	0.009	
D	_	0.118 BSC	_	
E	_	0.193 BSC	_	
E1	_	0.118 BSC	_	
е	_	0.020 BSC	_	
L	0.016	0.024	0.031	
L1	_	0.037 BSC	_	
у	_	0.004	_	
α	0°	_	8°	

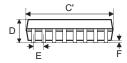
Cumbal	Dimensions in mm			
Symbol	Min.	Nom.	Max.	
A	_	_	1.10	
A1	0.00	_	0.15	
A2	0.75	0.85	0.95	
b	0.17	_	0.33	
С	0.08	_	0.23	
D	_	3.0 BSC	_	
E	_	4.9 BSC	_	
E1	_	3.0 BSC	_	
е	_	0.5 BSC	_	
L	0.40	0.60	0.80	
L1	_	0.95 BSC	_	
у	_	0.1	_	
α	0°	_	8°	

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### 16-pin NSOP (150mil) Outline Dimensions







Symbol	Dimensions in inch			
Symbol	Min.	Nom.	Max.	
А	_	0.236 BSC	_	
В	_	0.154 BSC	_	
С	0.012	_	0.020	
C'	_	0.390 BSC	_	
D	_	_	0.069	
E	_	0.050 BSC	_	
F	0.004	_	0.010	
G	0.016	_	0.050	
Н	0.004	_	0.010	
α	0°	_	8°	

Symbol	Dimensions in mm			
Symbol	Min.	Nom.	Max.	
A	_	6.0 BSC	_	
В	_	3.9 BSC	_	
С	0.31	_	0.51	
C'	_	9.9 BSC	_	
D	_	_	1.75	
E	_	1.27 BSC	_	
F	0.10	_	0.25	
G	0.40	_	1.27	
Н	0.10	_	0.25	
α	0°	_	8°	

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